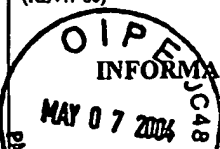


FORM PTO-1449 (REV. 7-80)	U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTY. DOCKET NO. 501193.01	APPLICATION NO. 10/601,252
 INFORMATION DISCLOSURE STATEMENT <i>(Use several sheets if necessary)</i>		APPLICANT(S) Terry R. Lee et al.	
		FILING DATE June 20, 2003	GROUP ART UNIT 2818

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
PB	AA	6,462,978 B2	10/08/02	Shibata et al.	365	63	
	AB						
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	AG						
	AH						
	AI						
	AJ						

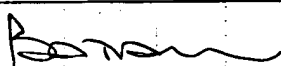
FOREIGN PATENT DOCUMENTS

		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
							YES	NO
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OTHER PRIOR ART *(Including Author, Title, Date, Pertinent Pages, Etc.)*

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EXAMINER



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1/6/05

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(REV.7-80)U.S. DEPARTMENT OF COMMERCE
PATENT AND TRADEMARK OFFICEATTY. DOCKET NO.
501193.01APPLICATION NO.
10/601,252

INFORMATION DISCLOSURE STATEMENT

(Use several sheets if necessary)

APPLICANT(S)

Terry R. Lee et al.

FILING DATE

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GROUP ART UNIT

2818

U.S. PATENT DOCUMENTS

*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
PB	AA	5,544,319	08/06/96	Acton et al.	395	200.07	
PB	AB	5,822,255	10/13/98	Uchida	365	194	
PB	AC	5,875,454	02/23/99	Craft et al.	711	113	
PB	AD	6,033,951	03/07/00	Chao	438	253	
PB	AE	6,079,008	06/20/00	Clery, III	712	11	
PB	AF	6,186,400 B1	02/13/01	Dvorkis et al.	235	462.45	
PB	AG	6,243,831 B1	06/05/01	Mustafa et al.	714	24	
PB	AH	6,256,692 B1	07/03/01	Yoda et al.	710	104	
PB	AI	6,392,653 B1	05/21/02	Malandain et al.	345	501	
PB	AJ	6,438,668 B1	08/20/02	Esfahani et al.	711	165	
PB	AK	6,636,110 B1	10/21/03	Ooishi et al.	327	565	
PB	AL	6,681,292 B2	01/20/04	Creta et al.	711	119	
	AM						
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OTHER PRIOR ART (Including Author, Title, Date, Pertinent Pages, Etc.)

PB	AU	"Free On-Line Dictionary of Computing" entry 'Flash Erasable Programmable Read-Only Memory, online May 17, 2004 [http://foldoc.doc.ic.ac.uk/foldoc/foldoc.cgi?flash+memory]
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<div style="display: flex; align-items: center; justify-content: center;"> <div style="border: 2px solid black; border-radius: 50%; padding: 10px; margin-right: 10px; text-align: center;"> O I P E FEB 24 2004 </div> <div> INFORMATION DISCLOSURE STATEMENT <i>(Use several sheets if necessary)</i> </div> </div>				APPLICANT(S) Terry R. Lee et al.			
				FILING DATE June 20, 2003		GROUP ART UNIT 2818	

U.S. PATENT DOCUMENTS							
*EXAMINER INITIAL	AA	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
PB	AA	5,818,844	10/06/98	Singh et al.	370	463	
PB	AB	6,272,609	08/07/01	Jeddeloh	711	169	
PB	AC	6,477,592	11/05/02	Chen et al.	710	52	
PB	AD	6,523,092	02/18/03	Fanning	711	134	
PB	AE	6,523,093	02/18/03	Bogin et al.	711	137	
PB	AF	6,622,227	09/16/03	Zumkehr et al.	711	167	
PB	AG	6,631,440	10/07/03	Jenne et al.	711	105	
PB	AH	2002/0144064	10/03/02	Fanning	711	144	
PB	AI	2003/0005223	01/02/03	Coulson et al.	711	118	
PB	AJ	2003/0229770	12/11/03	Jeddeloh	711	213	

FOREIGN PATENT DOCUMENTS							
		DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION
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OTHER PRIOR ART <i>(Including Author, Title, Date, Pertinent Pages, Etc.)</i>	
PB	AP Intel, "Intel 840 Chipset: 82840 Memory Controller Hub (MCH)", Datasheet, October 1999, pp. 1-178.

EXAMINER 	DATE CONSIDERED 11/6/05
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(REV. 7-80)U.S. DEPARTMENT OF COMMERCE
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INFORMATION DISCLOSURE STATEMENT

(Indicate relevant items if necessary)

APPLICANT(S)

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*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
PB	AA	6,067,262	05/23/00	Irrinki et al.	365	201	
PB	AB	6,246,618 B1	06/12/01	Yamamoto et al.	365	200	
PB	AC	6,590,816 B2	07/08/03	Perner	365	200	
PB	AD	2003/0043426 A1	03/06/03	Baker et al.	359	109	
	AE						
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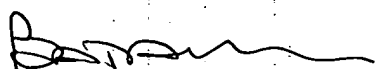
FORM PTO-1449 (REV. 7-80)	OCT 23 2003 U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE	ATTY. DOCKET NO. 501193.01	APPLICATION NO. 10/601,252
INFORMATION DISCLOSURE STATEMENT (Use several sheets if necessary)		APPLICANT(S) Terry R. Lee et al.	
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U.S. PATENT DOCUMENTS

*EXAMINER INITIAL		DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE
PB	AA	5,251,303	10/05/93	Fogg, Jr. et al.	395	275	
PB	AB	5,432,907	07/11/95	Picazo, Jr. et al.	395	200	
PB	AC	5,442,770	08/15/95	Barratt	395	403	
PB	AD	5,497,476	03/05/96	Oldfield et al.	395	439	
PB	AE	5,659,798	08/19/97	Blumrich et al.	395	846	
PB	AF	5,875,352	02/23/99	Gentry et al.	395	843	
PB	AG	6,061,263	05/09/00	Boaz et al.	365	51	
PB	AH	6,073,190	06/06/00	Rooney	710	56	
PB	AI	6,105,075	08/15/00	Ghaffari	710	5	
PB	AJ	6,137,709	10/24/00	Boaz et al.	365	51	
PB	AK	6,144,587	11/07/00	Yoshida	365	189.05	
PB	AL	6,167,465	12/26/00	Parvin et al.	710	22	
PB	AM	6,167,486	12/26/00	Lee et al.	711	120	
PB	AN	6,201,724 B1	03/13/01	Ishizaki et al.	365	49	
PB	AO	6,233,376 B1	05/15/01	Updegrove	385	14	
PB	AP	6,243,769 B1	06/05/01	Rooney	710	56	
PB	AQ	6,285,349 B1	09/04/01	Smith	345	147	
PB	AR	6,301,637 B1	10/09/01	Krull et al.	711	112	
PB	AS	6,327,642 B1	12/04/01	Lee et al.	711	120	
PB	AT	6,330,205 B2	12/11/01	Shimizu et al.	365	230.06	
PB	AU	6,347,055 B1	02/12/02	Motomura	365	189.05	
PB	AV	6,349,363 B2	02/19/02	Cai et al.	711	129	
PB	AW	6,356,573 B1	03/12/02	Jonsson et al.	372	46	
PB	AX	6,367,074 B1	04/02/02	Bates et al.	717	11	

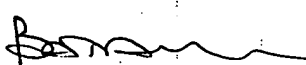
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INFORMATION DISCLOSURE STATEMENT <i>(Use several sheets if necessary)</i>				APPLICANT(S) Terry R. Lee et al.			
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U.S. PATENT DOCUMENTS							
*EXAMINER INITIAL	DOCUMENT NUMBER	DATE	NAME	CLASS	SUBCLASS	FILING DATE IF APPROPRIATE	
PB	AY	6,373,777 B1	04/16/02	Suzuki	365	230.03	
PB	AZ	6,381,190 B1	04/30/02	Shinkai	365	230.03	
PB	BA	6,421,744 B1	07/16/02	Morrison et al.	710	22	
PB	BB	6,434,639 B1	08/13/02	Haghighi	710	39	
PB	BC	6,463,059 B1	10/08/02	Movshovich et al.	370	389	
PB	BD	6,470,422 B2	10/22/02	Cai et al.	711	129	
PB	BE	6,473,828 B1	10/29/02	Matsui	711	104	
PB	BF	6,477,614 B1	11/05/02	Leddige et al.	711	5	
PB	BG	6,477,621 B1	11/05/02	Lee et al.	711	120	
PB	BH	6,496,909 B1	12/17/02	Schimmel	711	163	
PB	BI	6,505,287 B2	01/07/03	Uematsu	711	170	
PB	BJ	6,539,490 B1	03/25/03	Forbes et al.	713	401	
PB	BK	6,552,564 B1	04/22/03	Forbes et al.	326	30	
PB	BL	6,587,912 B2	07/01/03	Leddige et al.	711	5	
PB	BM	6,594,713 B1	07/15/03	Fuoco et al.	710	31	
	BN						
FOREIGN PATENT DOCUMENTS							
	DOCUMENT NUMBER	DATE	COUNTRY	CLASS	SUBCLASS	TRANSLATION	
						YES	NO
	BO						
OTHER PRIOR ART <i>(Including Author, Title, Date, Pertinent Pages, Etc.)</i>							
PB	BP	Micron Technology, Inc., "Synchronous DRAM Module 512MB/1GB (x72, ECC) 168-PIN Registered FBGA SDRAM DIMM, Micron Technology, Inc., 2002, pp. 1-23.					
	BQ						
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501193.01

APPLICATION NO.

INFORMATION DISCLOSURE STATEMENT*(Use several sheets if necessary)*

APPLICANT(S)

Terry R. Lee and Joseph Jeddeloh

FILING DATE

June 20, 2003

GROUP ART UNIT

U.S. PATENT DOCUMENTS

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PB	AA	2002/0112119	08/15/02	Halbert et al.	—	—	03/13/02
	AB						
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	AH						
	AI						
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